



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yoshihisa IWATA

SERIAL NO: 10/623,559

GAU:

FILED: July 22, 2003

EXAMINER:

FOR: SEMICONDUCTOR APPARATUS HAVING LOGIC LEVEL DECISION CIRCUIT AND INTER-  
SEMICONDUCTOR APPARATUS SIGNAL TRANSMISSION SYSTEM

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

## REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.

A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

## RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.

A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

## CERTIFICATION

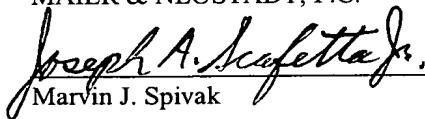
Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

## DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 240522US2S	SERIAL NO. 10/623,559		
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Yoshihisa IWATA					
		FILING DATE July 22, 2003		GROUP			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,349,302	09/20/94	L. K. COOPER			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
	AO				YES		NO
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	R. SCHEUERLEIN, et al., IEEE ISSCC Digest of Technical Papers, pages 128-129 (and 94-95 and 409-410), "A 10NS READ AND WRITE NON-VOLATILE MEMORY ARRAY USING A MAGNETIC TUNNEL JUNCTION AND FET SWITCH IN EACH CELL", February 2000					
	AX	R. CRISP, et al., IEEE ISSCC Digest of Technical Papers, pages 226-227 (and 176-177, 414-415 and 461), "DEVELOPMENT OF SINGLE-CHIP MULTI-GB/s DRAMs", February 7, 1997					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner				Date Considered			

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



### LIST OF RELATED CASES

<u>Docket Number</u>	<u>Serial or Patent Number</u>	<u>Filing or Issue Date</u>	<u>Inventor/Applicant</u>	<u>Client's Reference Number</u>
PER CLIENT	09/985,587	11/05/01	IWATA	G032813
240522US2S*	10/623,559	07/22/03	IWATA	T1NK-02S1434; ACG035271-USA-A

\*Present Application; listed for information  
EHK/sb